

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application : M. Selim Unlu et al.
Patent No. : 7,501,303
Issued : March 10, 2009
For : REFLECTIVE LAYER BURIED IN SILICON AND
METHOD OF FABRICATION
Attorney's Docket : BU-021AX

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L E T T E R

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In proofreading the above-identified patent as printed, the following errors were noted:

Column 2, line 1, "DRAWING" should read --DRAWINGS--;

Column 2, line 4, "drawing" should read --drawings--;

Column 2, line 13, "layer" should read --layer;--;

Column 3, line 4, "time" should read --times--;

Column 3, line 16, "has advantage" should read --has an advantage--;

Column 4, line 7, "phtotde-" should read --photode--;

Column 4, line 14, "tailered" should read --tailored--;
and

Column 4, line 21, "absortpion" should read
--absorption--.

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Since these errors do not appear to affect the sense of the text, a Certificate of Correction is not believed necessary. It is requested, however, that this letter be included with the official application file for record purposes.

Respectfully submitted,

M. Selim Unlu et al.

Date: December 15, 2009

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